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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/IB04/00752 03/16/2004

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

EUROPEAN PATENT OFFICE (EPO) 03100720.6 03/20/2003

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	<b>STATE OR COUNTRY</b> GERMANY	<b>SHEETS DRAWING</b> 2	<b>TOTAL CLAIMS</b> 6	<b>INDEPENDENT CLAIMS</b> 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>RES</i>		

## ADDRESS

24737

## TITLE

Circuit arrangement and transistor control method

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